

**EAST Search History****EAST Search History (Interference)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L19	0	((memory comprising) and (least one array of memory elements) and (partition at least one array into plurality sub-arrays memory elements) and (array configuration circuit selectively placing at least one array in one of two operating configurations) and (two operating configurations including) and (first operating configuration memory elements at least one array coupled one to another form monodimensional sequentially-accessible memory) and (second operating configuration memory elements each sub- array coupled one another form independent monodimensional sequentially-accessible memory block) and (memory blocks each sub-array being isolated memory blocks other sub-arrays) and (data content any memory element sub- array being rotatable shifts through memory elements sub- array) and (sub-array selector responsive first memory address) and (selecting one among plurality sub-arrays according first memory address sub-	US- PGPUB; USPAT; UPAD	SAME	ON	2010/06/19 17:40

array selector enabling access selected sub-array) and (memory element access circuit responsive second memory address enabling access prescribed memory element selected sub-array after prescribed number shifts depending second memory address data content memory elements selected sub-array) and (first operating configuration data storage configuration memory placed data stored therein) and (second operating configuration data retrieval configuration memory placed data retrieved therefrom)).clm.

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